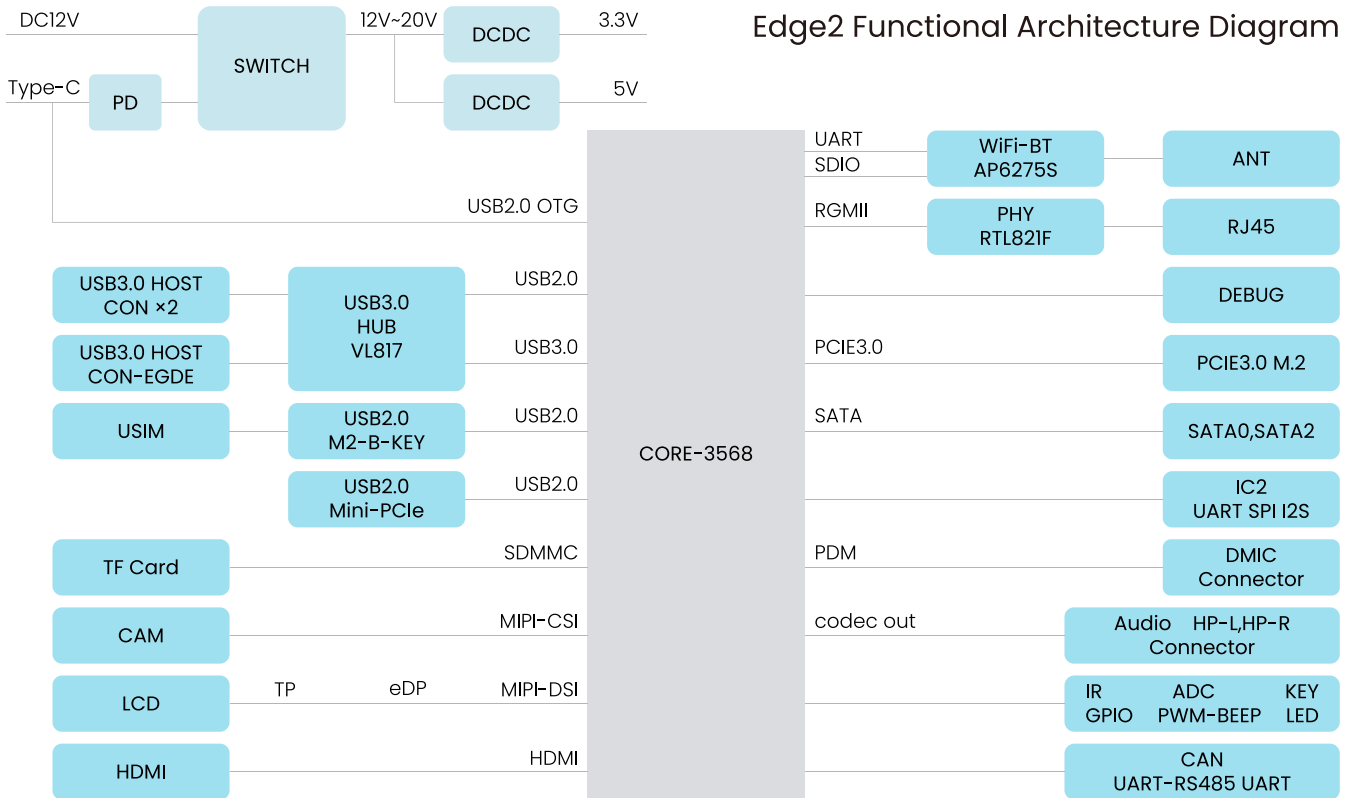


Edge 2 is a Mixtile designed high performance and low power single board computer targeted for commercial IoT networking & computing applications. The SBC features the Rockchip Quad-core Cortex-A55 SoC Processor up to 2.0GHz, 2GBytes of LPDDR4 DRAM, and 16GBytes of eMMC Flash Memory Standard provided by Mixtile System-on-Module Core 3568. High-power M.2 and Mini-PCIe expansion sockets support a combination of LTE/5G/4G/3G cellular modules, LoRa, Zigbee, Z-Wave, and other wireless technologies modules. Additional interface, U.2 expansion port, contains SATA/PCIe/USB3.0/RGMII that provides more probability of application. As for the power supply, USB Power Delivery from 5 to 20VDC and normal 12VDC supply offer two methods of power supplying for different scenarios.

## Features

- Rockchip Quad-core Cortex-A55 SoC Processor RK3568
- NPU 0.8Tops
- 2GBytes LPDDR4 DRAM Memory and 16GBytes eMMC Flash Memory Standard
- HDMI2.0 / eDP / MIPI-DSI + TP / MIPI-DSI / MIPI-CSI
  - 4KP60 H.265/H.264/VP9 video decoder
  - 1080P100 H.264/H.265 video encoder
- WiFi 6 802.11ax
- Bluetooth 5.0
- Support 4G/5G module
- Gigabit Ethernet Ports
- Micro-SD 3.0 Flash Expansion Socket
- M.2 Socket: PCIe 3.0, USB2.0, SIM Socket
- Mini-PCIe Socket: USB2.0, SIM Socket
- Three USB3.0 ports
- One RS485 and two TTL
- Digital I/O, SPDIF, I2C, DMIC
- U.2 Expansion Socket: SATA or PCIe 2.0, SATA, Gigabit Ethernet
- Real Time Clock with Battery Backup
- Two power supply port:
  - 12V DC / SATA power socket
  - Type-C PD3.0 Power input (max 3A @ 20V)
- 0 to +80°C Operating Temperature





## Electrical:

- Input Voltage:
  - 5 to 20VDC PD Type-C
  - 12VDC DC jack / SATA power
- Output Voltage:
  - 5VDC
- Operating Current:
  - Maximum 3A @ 20VDC

## Mechanical

- Dimensions: 100mm x 140mm

## Environmental

- Operating Parameters
  - Temperature: 0 to +80°C

## Development kit

- Mixtile Edge 2
- Android 11 Board Support Package
- Ethernet, USB Cables
- Two WiFi antennas

## Product Customization

- System-on-Module Pin to Pin family
- LPDDR4 DRAM from 2GBytes
- System Flash from 16GBytes
- Adding and removing interfaces